



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kowalski, *et al.* Docket No.: INF-125
Serial No.: 10/748,332 Art Unit: 2815
Filed: December 31, 2003 Examiner: Matthew E. Warren
For: Device Architecture and Process for Improved Vertical Memory Arrays

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: June 17, 2005

I hereby certify that the below listed correspondence is being deposited with the United States Postal Service on the date indicated above as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Certificate of Mailing via First Class Mail (1 page)
Part B - Fee Transmittal (1 original and 1 copy)
Transmittal of Replacement Sheets of Formal Drawings (1 page)
One (1) Replacement Sheet of Formal Drawings
Return Postcard

Respectfully submitted,

Kristin R. Hayes
Legal Assistant

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Mail Stop Issue Fee
Commissioner for Patents
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Alexandria, VA 22313-1450

TRANSMITTAL OF REPLACEMENT SHEETS OF FORMAL DRAWINGS

Dear Sir:

As requested by the Examiner in the Office Action dated November 5, 2004, please amend Figures 1 and 2 to include the legend "Prior Art." Consequently, submitted herewith is one (1) replacement sheet of formal drawings for the above-identified application.

Respectfully submitted,

Ira S. Matsil
Attorney for Applicants
Reg. No. 35,272

June 17, 2005
Date

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